10

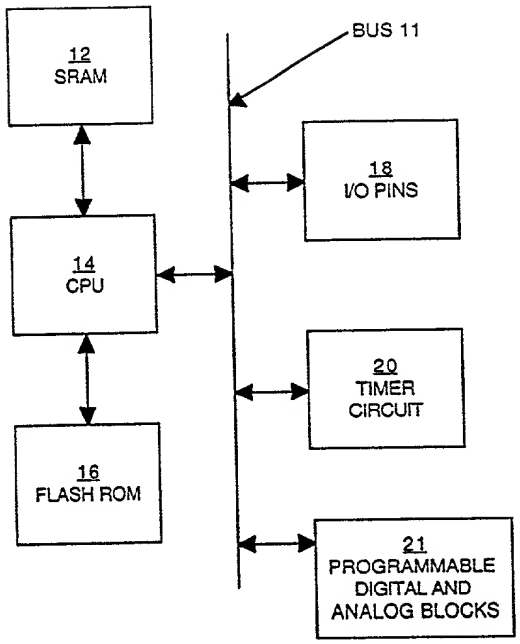

12
SRAM
BUS 11
18
I/O PINS
14
CPU
20
TIMER
CIRCUIT
16
FLASH ROM
21
PROGRAMMABLE
DIGITAL AND
ANALOG BLOCKS

Figure 1

Figure 2

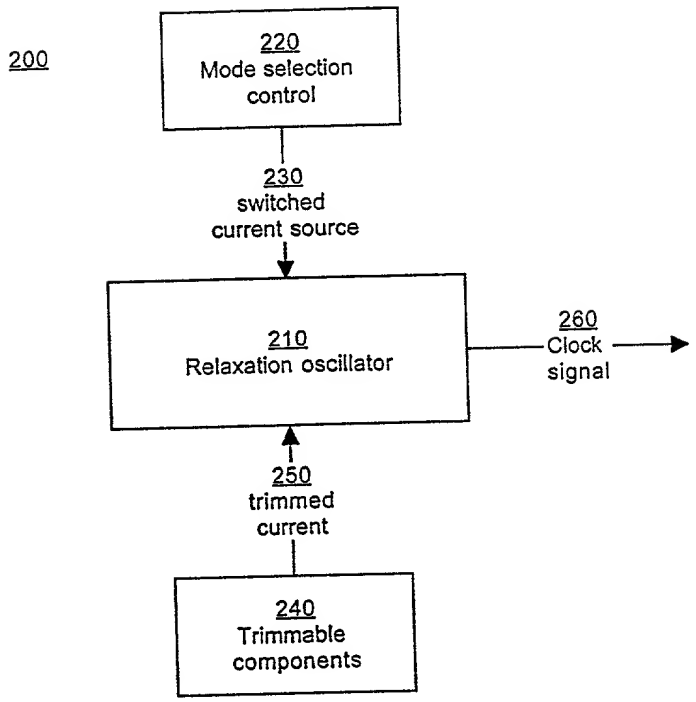
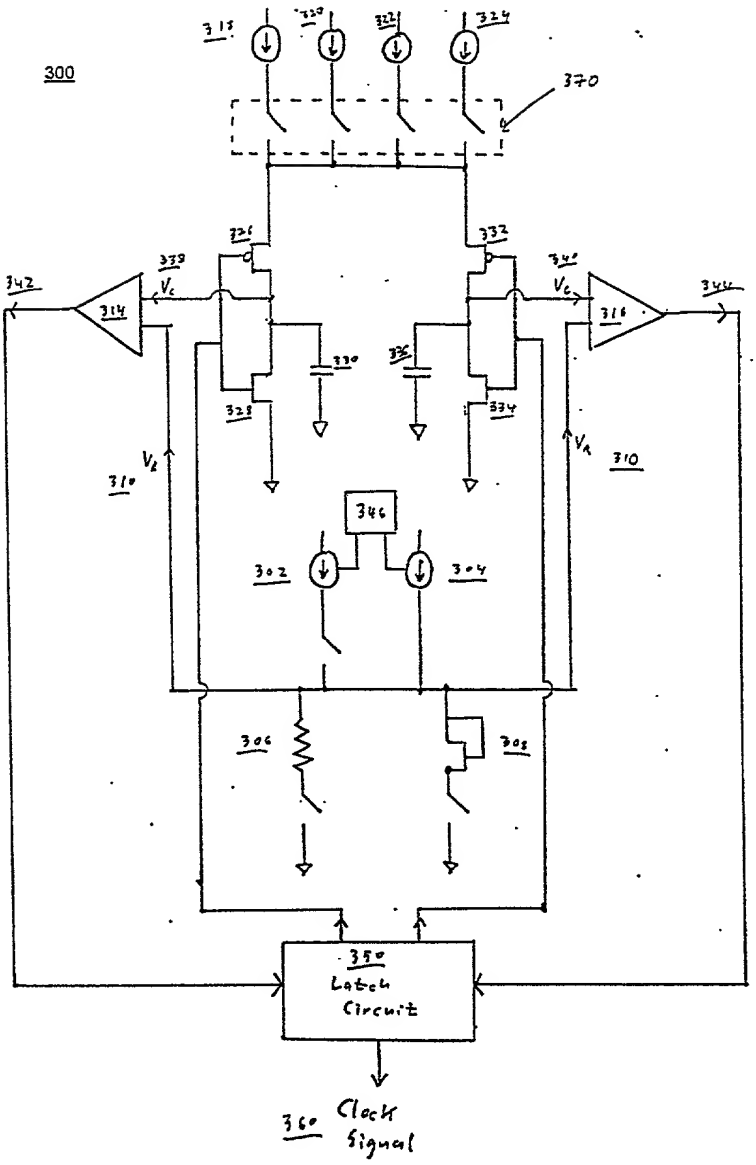

200
220
Mode selection control
230
switched current source
210
Relaxation oscillator
260
Clock signal
250
trimmed current
240
Trimmable components

Figure 3


300
315
320
322
324
370
326
332
339
$V_c$
342
314
330
328
334
$V_c$
316
344
$V_A$
310
$V_L$
310
346
302
304
306
308
350
Latch Circuit
360 Clock Signal

TOH220 8923660

STAEDTLER® No. 937 811E
Engineer's Computation Pad

Figure 4

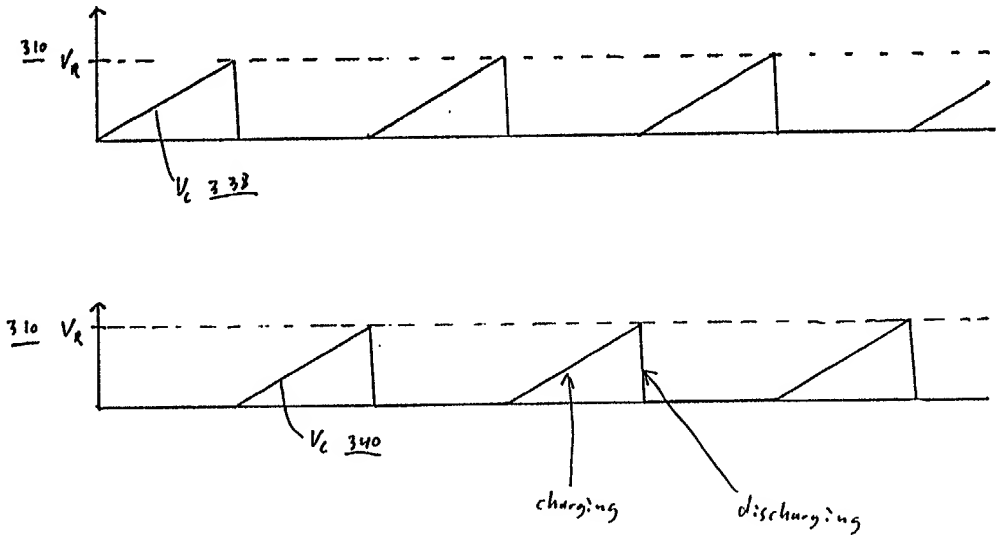
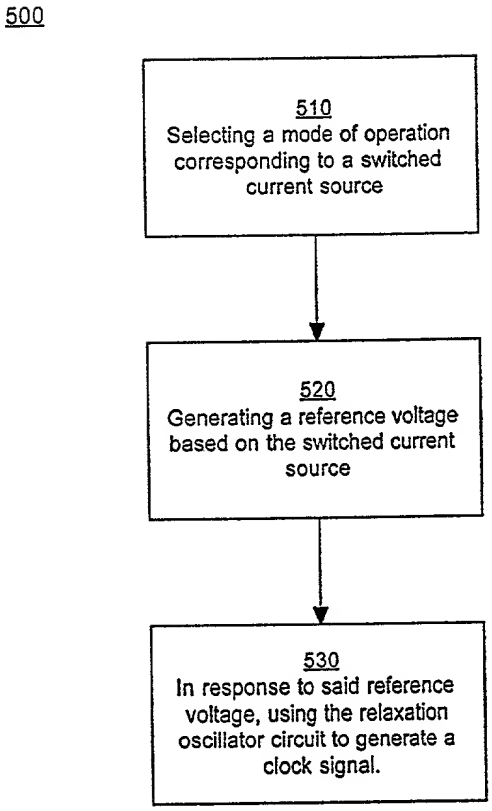


Figure 5


500
510
Selecting a mode of operation corresponding to a switched current source
520
Generating a reference voltage based on the switched current source
530
In response to said reference voltage, using the relaxation oscillator circuit to generate a clock signal.